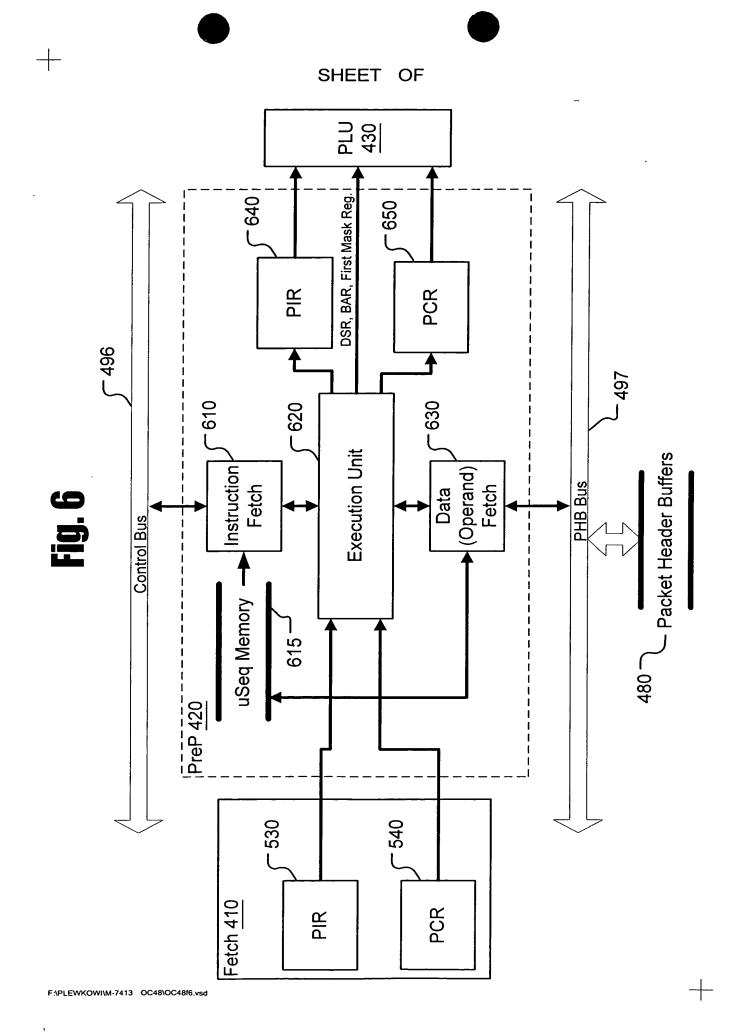
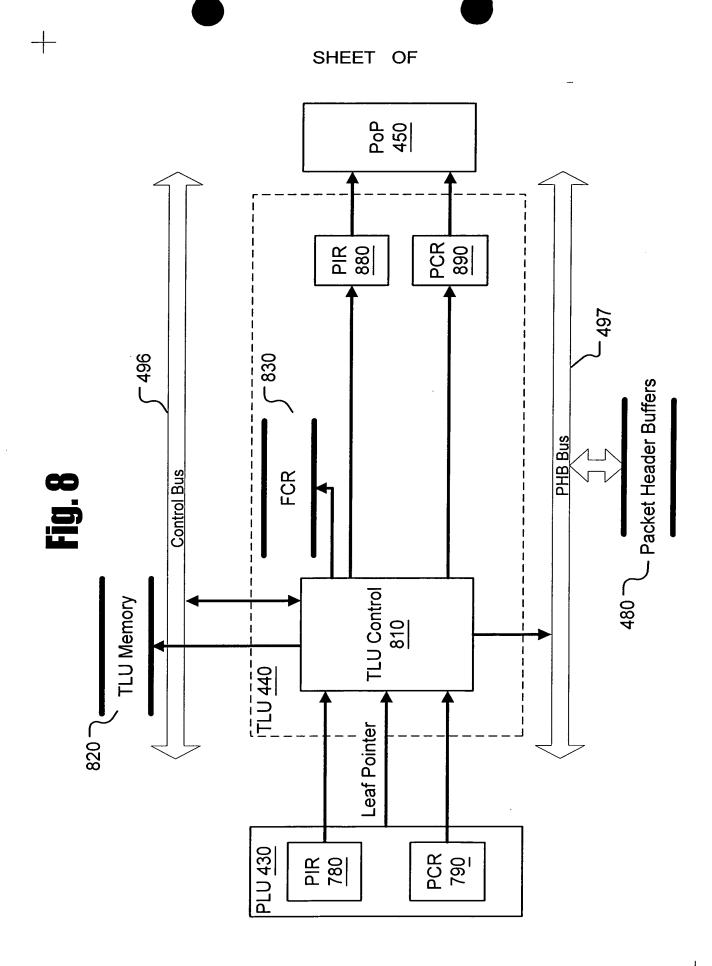
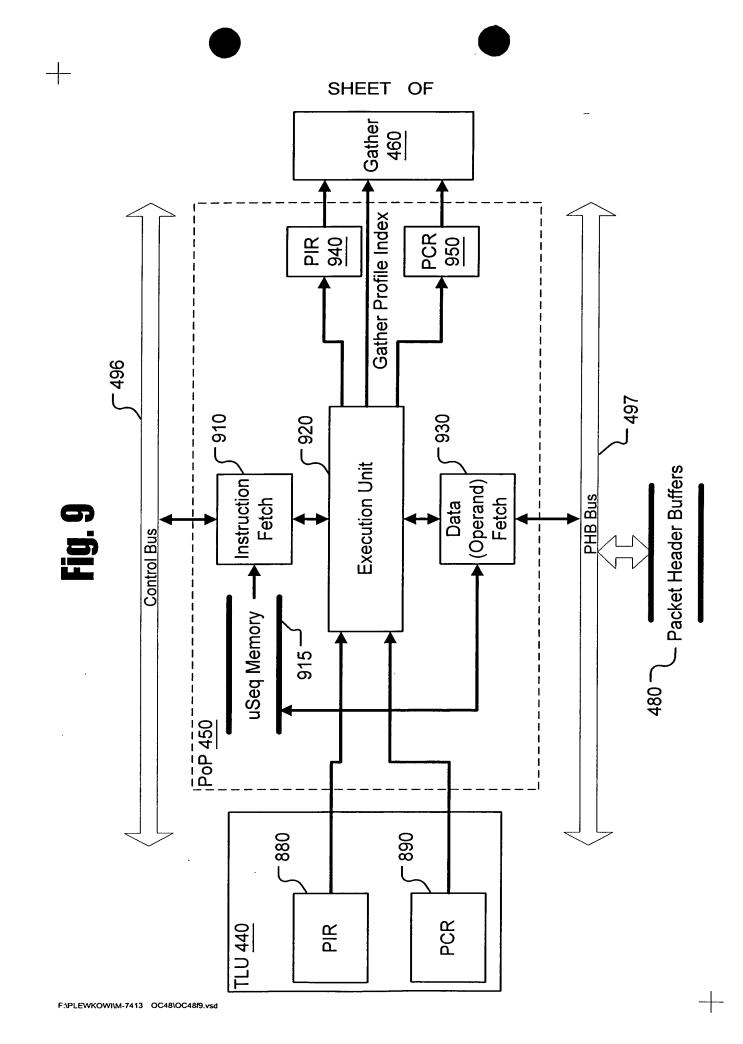


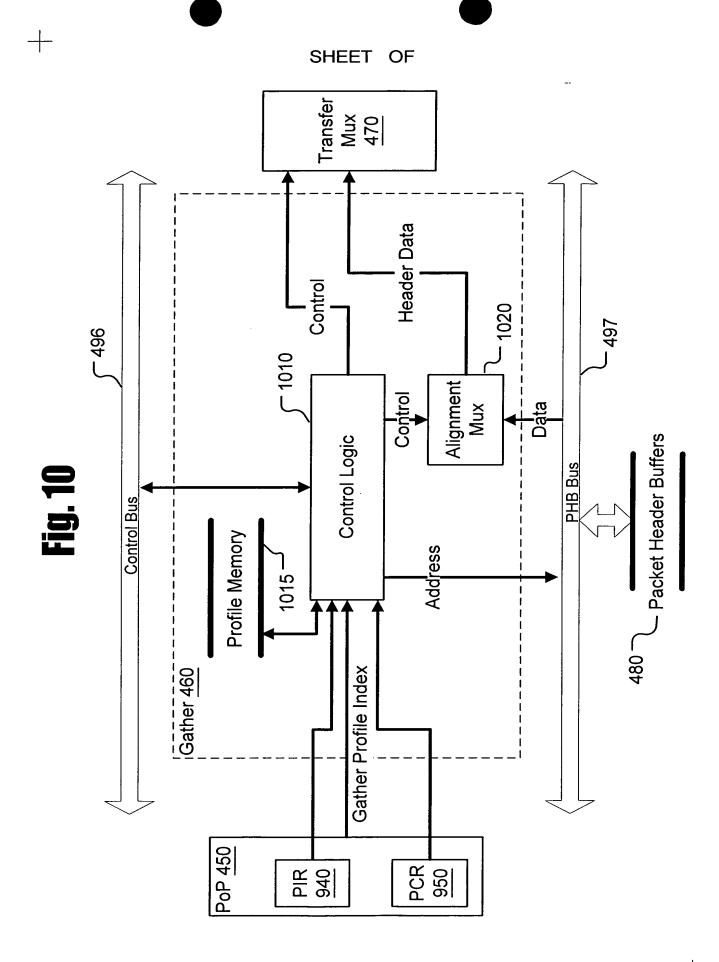
SHEET OF PreP 420 - 540 - 530 **PCR** 몺 - 496 497 510 Header Portions Packet Header Buffers PHB Bus Validator/ Classifier Checksum L2 Header Validator Control Bus 520 Fetch 410 Header Portions Receive FIFO 215.



SHEET OF 7LU 440 PCR 790 PIR 780 Leaf Pointer 496 Control Bus PLU Control PLU Memory PLU 430 DSR, BAR, First Mask Reg. PreP 420 PCR 650 PIR 640







SHEET OF

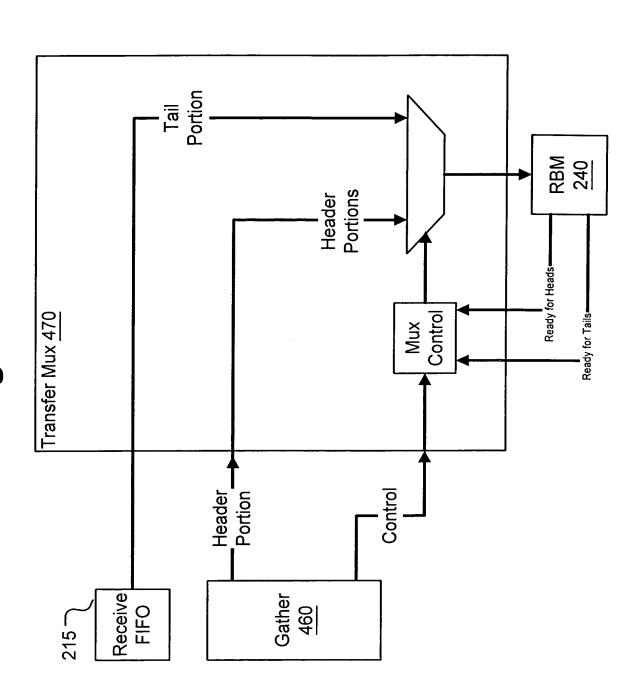
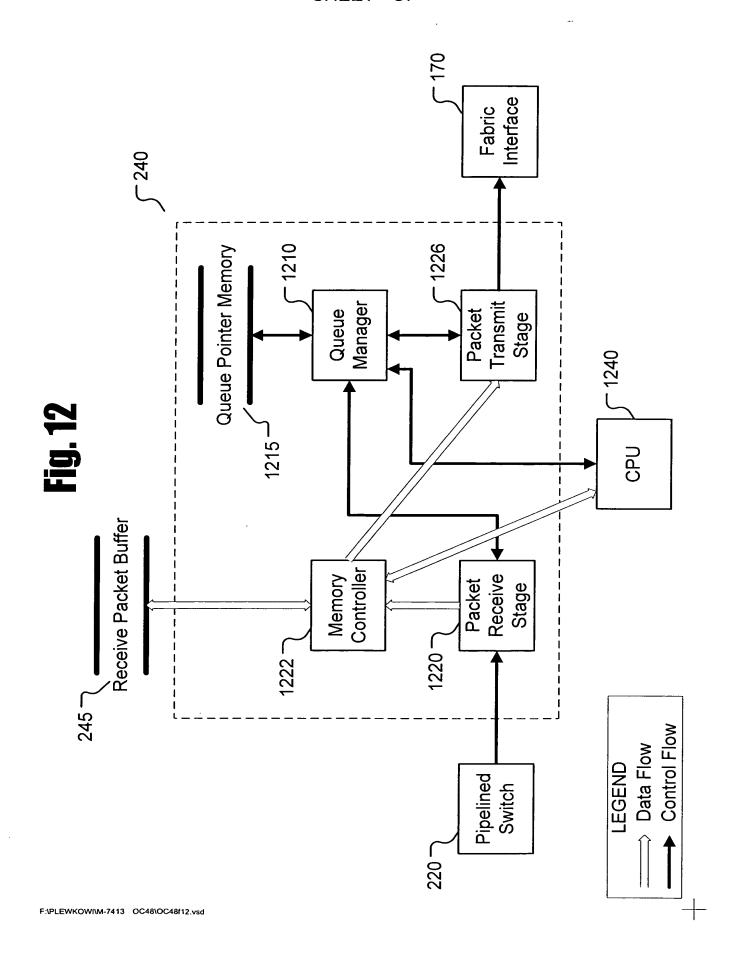
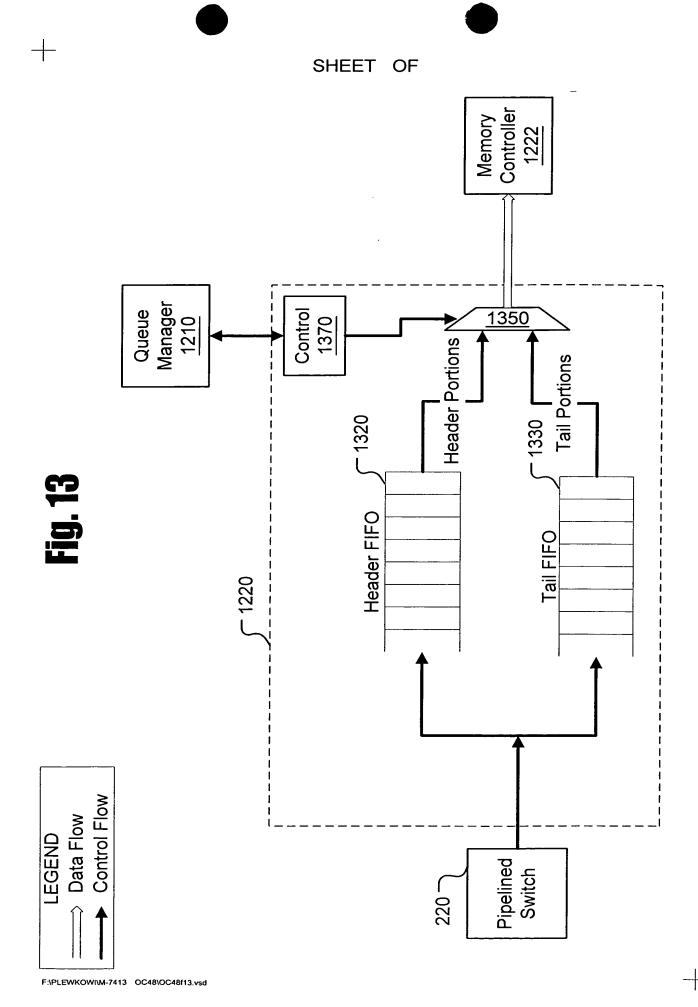


Fig. 11

SHEET OF



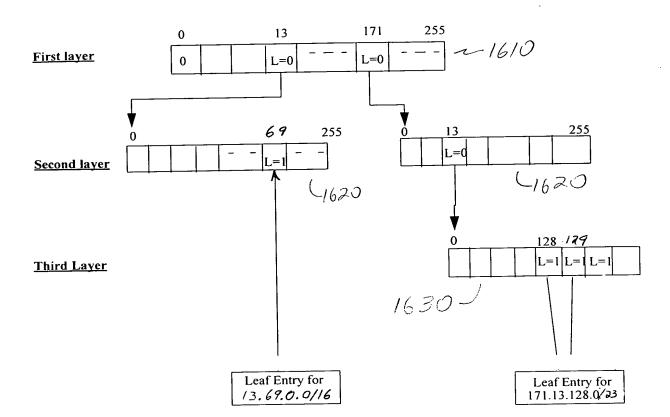


SHEET OF - 1210 1226 1460 1470 Packet Transmit Stage OQ DQ MDRR . 1405 1430 Queue Pointer Memory FQ EQ 1450 Link Manager Fig. 14 Arbitration CPU 1240 FQ DQ 1440 1490 OQ EQ RED **1410** 一 Packet Receive Stage

Fig. 15

SHEET OF Transmit FIFO - 280 .1510 1526 Queue Pointer Memory Queue Manager Packet Transmit Stage 1515 -CPU **Fransmit Packet Buffer** Controller Packet Receive Memory Stage 1520 -1522 -**Control Flow** Data Flow LEGEND Interface Fabric

● #IG. 16 ●



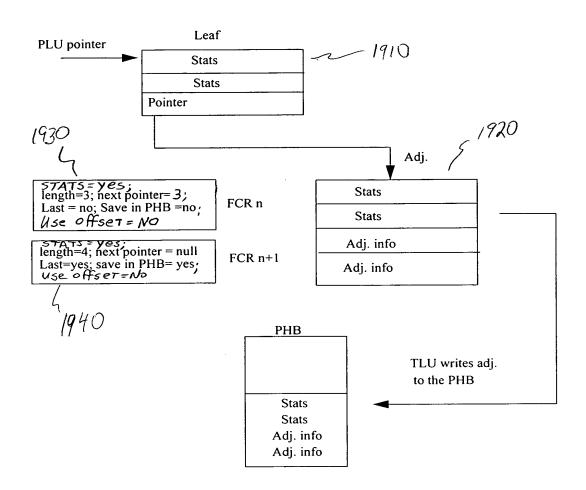
Offset 171 256 ₹256 13 69 0 13 0 0 L=0; 0xDDDDDD00 L=1; pointer to leaf L=0; 0xCCCCC00 L=0; 0xBBBBB00 Other layer 2 entries Next Address shift right Base Address Register (BAR) Pointer to top of tree 0xFF000000 Mask III register 0x00FF0000 Mask IV register 0x000000FF 0x0000FF00 0xAAAA00 Mask II register Mask I register 171.13.128.0

layer 3 starts here 256 layer 2 starts here 0xDDDDDD80 0xDDDDD81 0xBBBBBB00 0xDDDDD00 0xcccc0D 0xAAAAAB 0xAAAA00 0xccccc00 Address 256 12**8** 129 0 L=1; Points to leaf L=1; Points to leaf to the same address as both satisfy the /23 Both pointers point Mask select Destination Search Reg (DSR) Mask VIII register 0x000000FF

0xAAFFFFC 0xAA000000 data out TAG look up table in the PLU L=1_pointer Ξ = [=] = [=] pointer to TLU 0xAA000004 memory. shift right Mask select=VIII Base Address Register (BAR) Destination Search Reg (DSR) 0xAA0000000 Pointer to top of tree 0x000004XXX Mask VIII register 0xFF000000 Mask III register 0xFFFFF000 0x00FF0000 0x0000FF00 Mask IV register 0x000000FFMask II register Mask I register

oosossa subject





#IG. 20 •

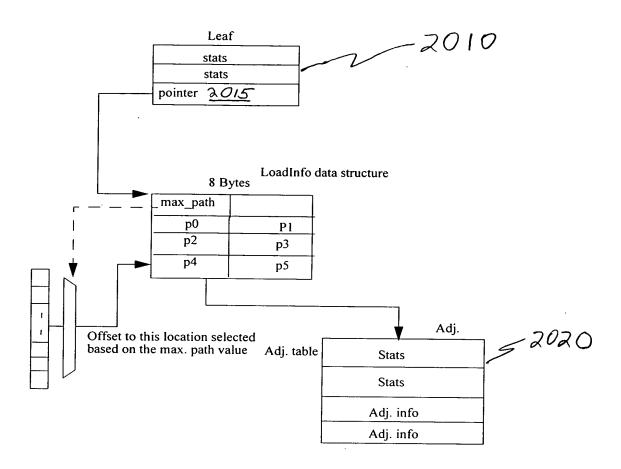


FIG. 21

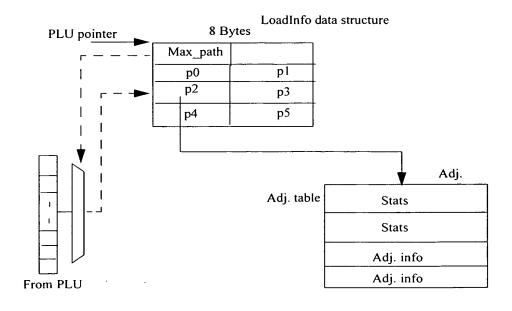
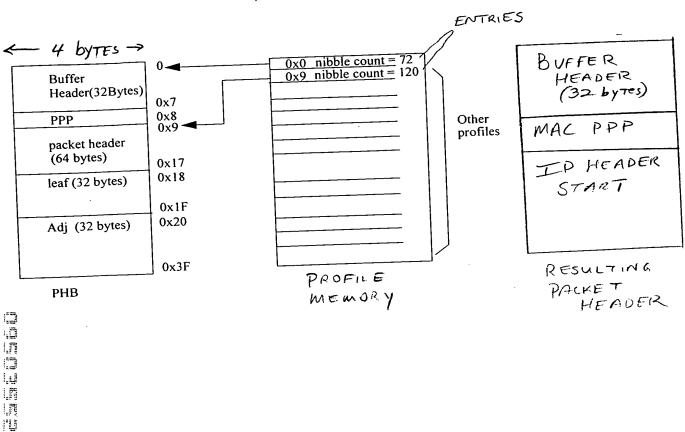
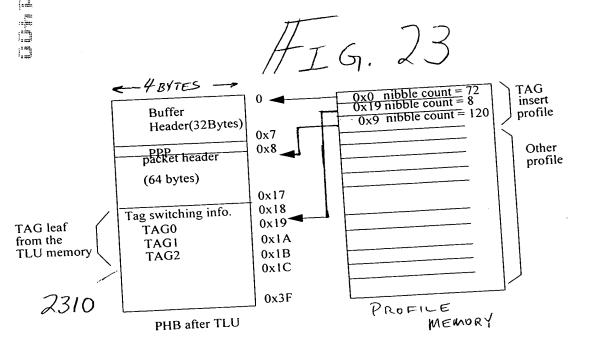


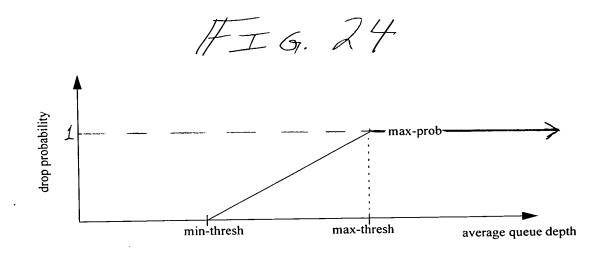
FIG. 22

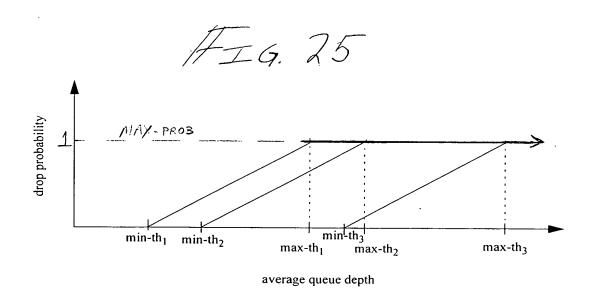




Resulting packet header in RBM:

	1
Buffer Header	
(32 bytes)	+
mac PPP	4
TAG 0	
IP header start here	١
	ļ
	١





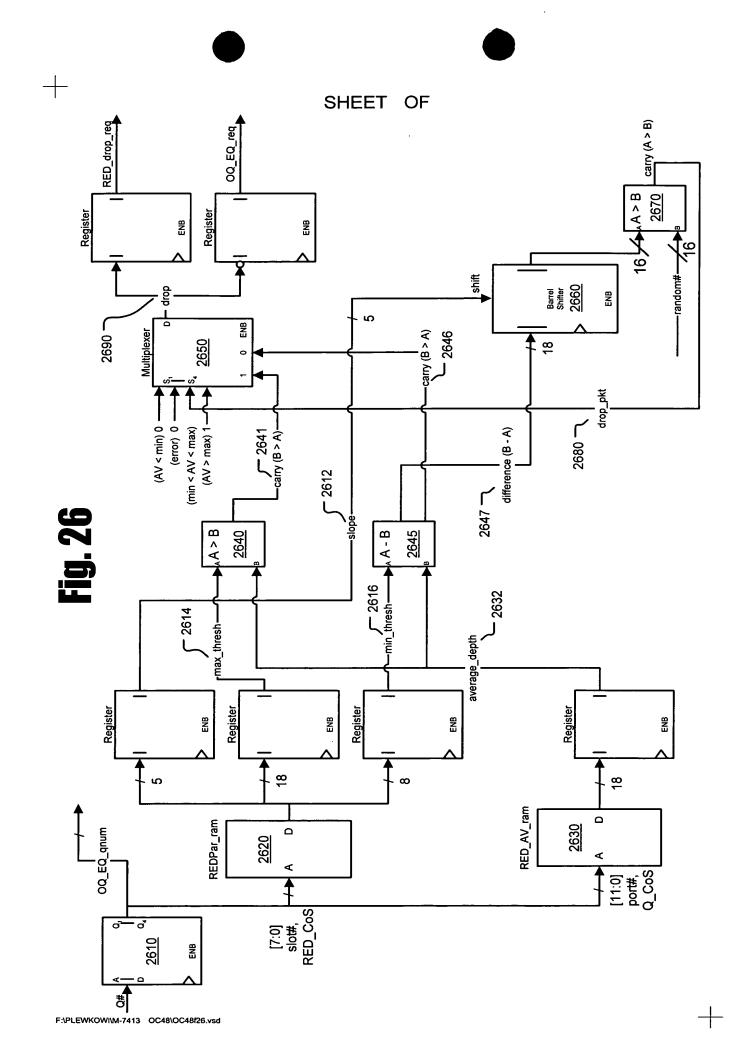
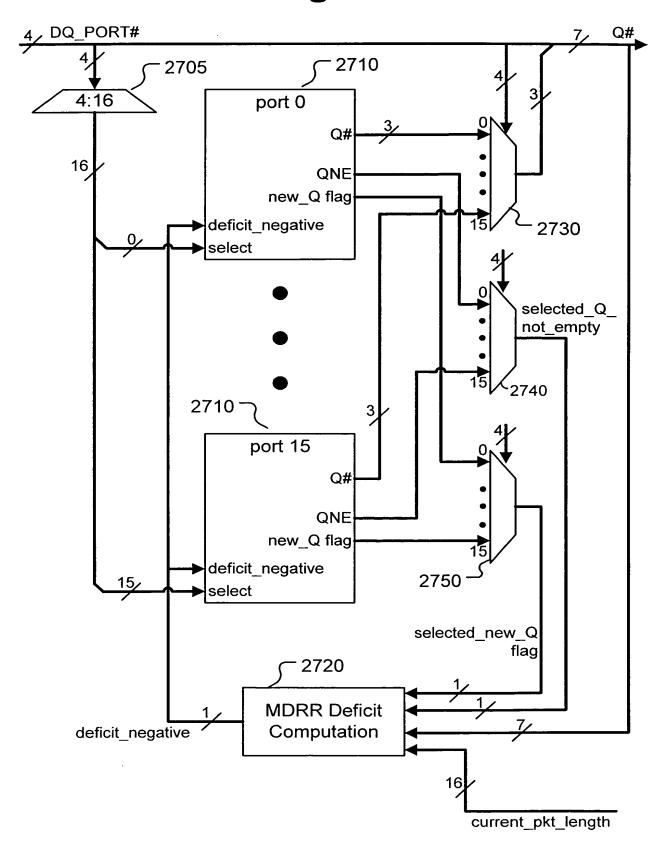
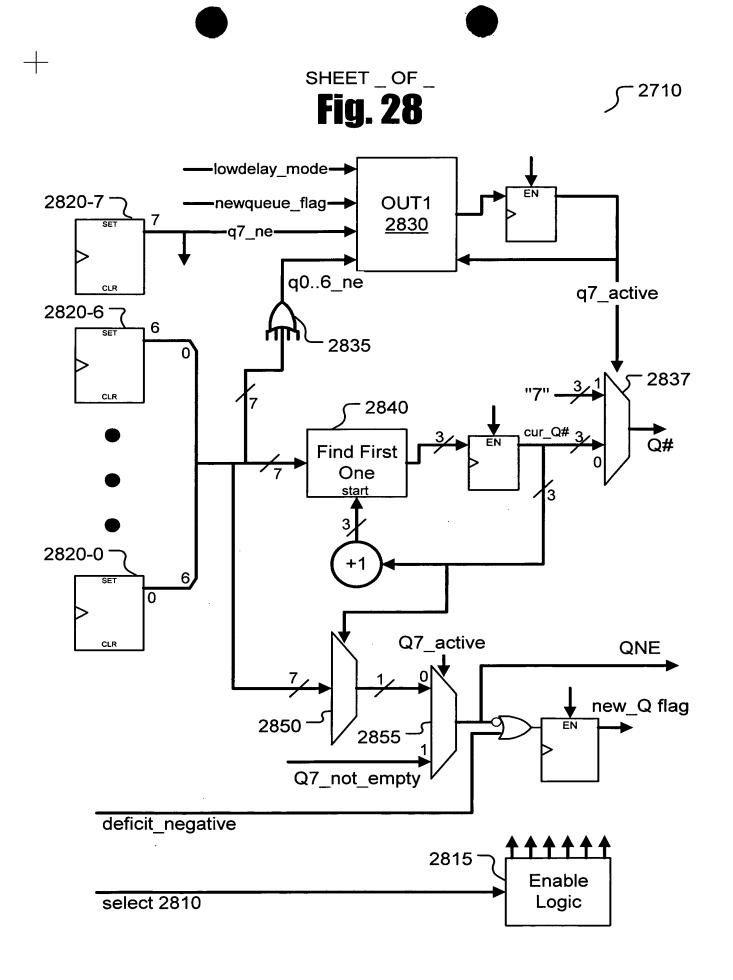
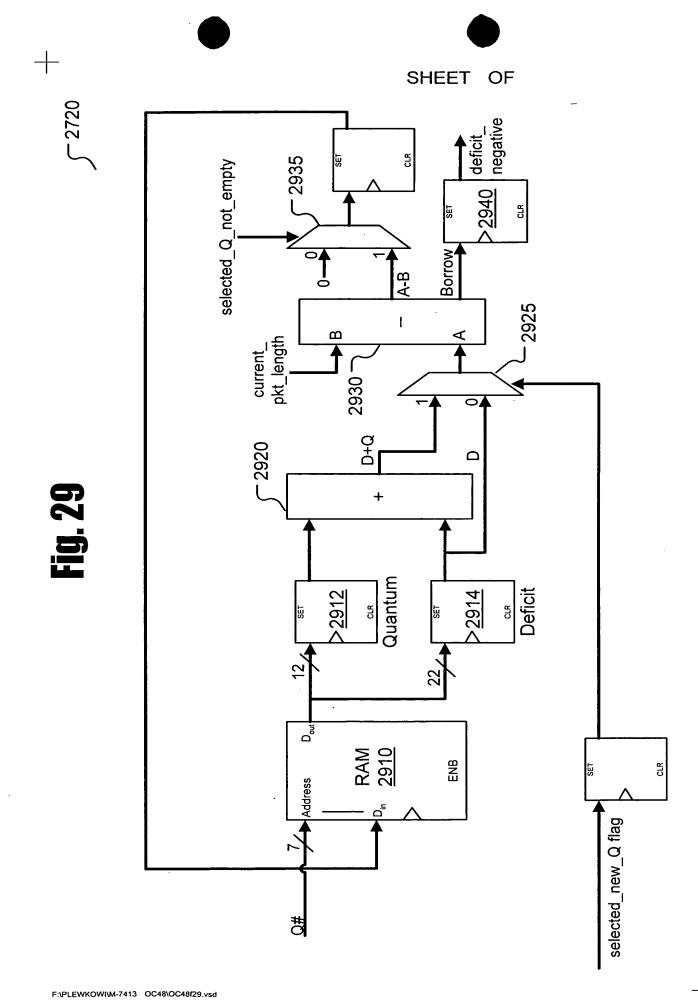


Fig. 27

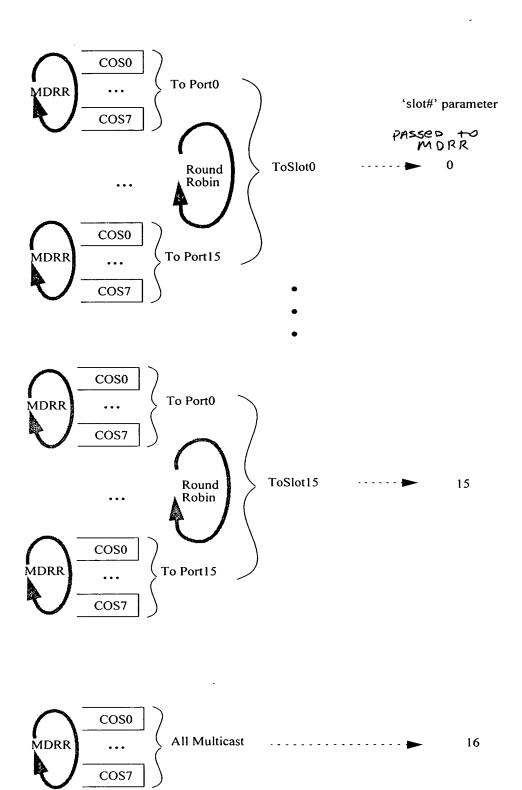


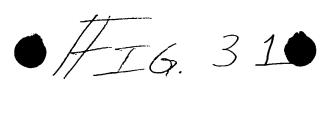


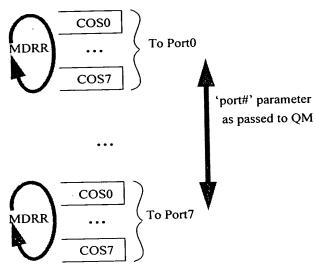












SHEET OF

